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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of)
Shunpei YAMAZAKI)
Serial No. 09/583,087)
Filed: February 1, 2000)
For: ELECTRO-OPTICAL DEVICE)
AND METHOD FOR)
MANUFACTURING THE SAME)

Art Unit: 2815
Examiner: J. Jackson, Jr.

<p align="center">CERTIFICATE OF MAILING</p> <p>I hereby certify that this correspondence is being deposited with The United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on <u>8.6.03</u></p> <p align="right"><i>Adelle M. Stanger</i></p>

RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

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The Official Action mailed March 6, 2003, has been received and its contents carefully noted. Filed concurrently herewith is a *Request for Two Month Extension of Time*, which extends the shortened statutory period for response to August 6, 2003. Accordingly, the Applicant respectfully submits that this response is being timely filed.

The Applicant notes with appreciation the consideration of the Information Disclosure Statements filed on September 5, 2000, February 15, 2001, March 15, 2001, March 22, 2001, August 15, 2001, November 2, 2001, June 21, 2002, October 16, 2002, and December 20, 2002. The Applicant awaits consideration of the Information Disclosure Statement filed March 3, 2003. Also, a further IDS is submitted herewith and careful review and consideration of this IDS is requested.


Claims 2-41 and 43-90 are now pending in the present application, of which claims 21-32 and 41 are independent. For the reasons set forth in detail below, all claims are believed to be in condition for allowance.

The Official Action rejects claims 21-41 and 43-90 under the doctrine of obviousness-type double patenting over claims 1-46 of U.S. Patent No. 6,023,075 to Yamazaki in view of JP 2-234134 to Sumiyoshi, the article *An Active-Matrix LCD with Integrated Driver Circuits Using a-Si TFTs* by Akiyama, and U.S. Patent No. 5,656,826 to Misawa. While Applicants continue to traverse this rejection for the reasons of record

previously submitted, Applicants are in any event preparing a Terminal Disclaimer and will submit the Disclaimer as soon as it is received from Japan. With the submission of this Terminal Disclaimer, the double patenting rejection is believed to be overcome and reconsideration is requested.

Should the Examiner believe that anything further would be desirable to place this application in better condition for allowance, the Examiner is invited to contact the Applicant's undersigned attorney at the telephone number listed below.

Respectfully submitted,



Eric J. Robinson
Reg. No. 38,285

Robinson Intellectual Property Law Office, P.C.
PMB 955
21010 Southbank Street
Potomac Falls, Virginia 20165
(571) 434-6789